I B.Tech II Sem

COURSE NO. - **Basic Electrical & Electronics Engineering Lab**

(Common to Civil, Mechanical, CSE)

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PART A: ELECTRICAL ENGINEERING LAB

Course Objectives:
- To Verify Kirchoff's laws
- To verify Superposition theorem.
- To learn performance characteristics of DC Machines.
- To perform open circuit & Short Circuit test on 1- Phase Transformer.
- To Study the I – V Characteristics of Solar PV Cell

List of experiments: -
1. Verification of Kirchhoff laws.
2. Verification of Superposition Theorem.
3. Open circuit characteristics of a DC Shunt Generator.
4. Speed control of DC Shunt Motor.
5. OC & SC test of 1 – Phase Transformer.
6. Brake test on 3 - Phase Induction Motor.
7. I – V Characteristics of Solar PV cell
8. Brake test on DC Shunt Motor.

Course Outcomes: Able to
- Verify Kirchoff's Laws & Superposition theorem.
- Perform testing on AC and DC Machines.
- Study I – V Characteristics of PV Cell

PART B: ELECTRONICS ENGINEERING LAB

Course outcomes:
- Describe construction, working and characteristics of diodes, transistors and operational amplifiers
- Demonstrate how electronic devices are used for applications such as rectification, switching and amplification
- Build different building blocks in digital electronics using logic gates
- Explain functionality of flip-flops, shift registers and counters for data processing applications

- Explain functioning of various communication systems

LIST OF EXPERIMENTS:

1. Draw and study the characteristics of Semi-conductor diode and Zener Diode
2. Draw and study the input and output characteristics of Transistor in Common Emitter configuration
3. Draw and study the static and transfer characteristics of FET in Common Source Configuration
4. Construct half wave and full wave rectifier circuits. Find ripple factor and plot their output waveforms with and without filters
5. Study the application of Op-amp as an Inverting amplifier, Non-inverting amplifier, Voltage follower, Summer and Subtractor
6. Realization of logic gates, AND, OR, NOT, NAND, NOR, XOR
7. Realization of Adders, Multiplexers and Decoders using logic gates.
8. Realization of flip-flops using logic gates.
9. Conduct an experiment on AM & FM modulation & demodulation, Plot the corresponding modulated and demodulated signals